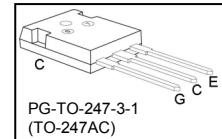
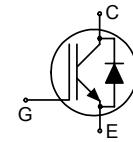


Fast IGBT in NPT-technology with soft, fast recovery anti-parallel EmCon diode

- 75% lower E_{off} compared to previous generation combined with low conduction losses
- Short circuit withstand time – 10 μs
- Designed for:
 - Motor controls
 - Inverter
- NPT-Technology for 600V applications offers:
 - very tight parameter distribution
 - high ruggedness, temperature stable behaviour
 - parallel switching capability
- Very soft, fast recovery anti-parallel EmCon diode
- Qualified according to JEDEC¹ for target applications
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models : <http://www.infineon.com/igbt/>



Type	V_{CE}	I_C	$V_{CE(\text{sat})}$	T_j	Marking	Package
SKW30N60	600V	30A	2.5V	150°C	K30N60	PG-TO-247-3-21

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage $T_C = 25^\circ\text{C}$	V_{CE}	600	V
DC collector current $T_C = 25^\circ\text{C}$	I_C	41	A
$T_C = 100^\circ\text{C}$		30	
Pulsed collector current, t_p limited by $T_{j\text{max}}$	$I_{C\text{puls}}$	112	
Turn off safe operating area $V_{CE} \leq 600\text{V}, T_j \leq 150^\circ\text{C}$	-	112	
Diode forward current $T_C = 25^\circ\text{C}$	I_F	41	
$T_C = 100^\circ\text{C}$		30	
Diode pulsed current, t_p limited by $T_{j\text{max}}$	$I_{F\text{puls}}$	112	
Gate-emitter voltage	V_{GE}	± 20	V
Short circuit withstand time ² $V_{GE} = 15\text{V}, V_{CC} \leq 600\text{V}, T_j \leq 150^\circ\text{C}$	t_{SC}	10	μs
Power dissipation $T_C = 25^\circ\text{C}$	P_{tot}	250	W
Soldering temperature w wavesoldering, 1.6 mm (0.063 in.) from case for 10s	T_s	260	$^\circ\text{C}$
Operating junction and storage temperature	T_j, T_{stg}	-55...+150	$^\circ\text{C}$

¹ J-STD-020 and JESD-022

² Allowed number of short circuits: <1000; time between short circuits: >1s.

Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic				
IGBT thermal resistance, junction – case	R_{thJC}		0.5	K/W
Diode thermal resistance, junction – case	R_{thJCD}		1	
Thermal resistance, junction – ambient	R_{thJA}		40	

Electrical Characteristic, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	Typ.	max.	
Static Characteristic						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE}=0\text{V}, I_C=500\mu\text{A}$	600	-	-	V
Collector-emitter saturation voltage	$V_{CE(\text{sat})}$	$V_{GE} = 15\text{V}, I_C=30\text{A}$ $T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	1.7 -	2.1 2.5	2.4 3.0	
Diode forward voltage	V_F	$V_{GE}=0\text{V}, I_F=30\text{A}$ $T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	1.2 -	1.4 1.25	1.8 1.65	
Gate-emitter threshold voltage	$V_{GE(\text{th})}$	$I_C=700\mu\text{A}, V_{CE}=V_{GE}$	3	4	5	
Zero gate voltage collector current	I_{CES}	$V_{CE}=600\text{V}, V_{GE}=0\text{V}$ $T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	- -	-	40 3000	μA
Gate-emitter leakage current	I_{GES}	$V_{CE}=0\text{V}, V_{GE}=20\text{V}$	-	-	100	nA
Transconductance	g_{fs}	$V_{CE}=20\text{V}, I_C=30\text{A}$	-	20	-	S

Dynamic Characteristic

Input capacitance	C_{iss}	$V_{CE}=25\text{V},$ $V_{GE}=0\text{V},$ $f=1\text{MHz}$	-	1600	1920	pF
Output capacitance	C_{oss}		-	150	180	
Reverse transfer capacitance	C_{rss}		-	92	110	
Gate charge	Q_{Gate}	$V_{CC}=480\text{V}, I_C=30\text{A}$ $V_{GE}=15\text{V}$	-	140	182	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	L_E		-	13	-	nH
Short circuit collector current ¹⁾	$I_{C(\text{SC})}$	$V_{GE}=15\text{V}, t_{sc} \leq 10\mu\text{s}$ $V_{CC} \leq 600\text{V},$ $T_j \leq 150^\circ\text{C}$	-	300	-	A

¹⁾ Allowed number of short circuits: <1000; time between short circuits: >1s.

Switching Characteristic, Inductive Load, at $T_j=25^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	$T_j=25^\circ\text{C}$, $V_{CC}=400\text{V}$, $I_C=30\text{A}$, $V_{GE}=0/15\text{V}$, $R_G=11\Omega$, $L_\sigma^{(1)}=180\text{nH}$, $C_\sigma^{(1)}=900\text{pF}$ Energy losses include “tail” and diode reverse recovery.	-	44	53	ns
Rise time	t_r		-	34	40	
Turn-off delay time	$t_{d(off)}$		-	291	349	
Fall time	t_f		-	58	70	
Turn-on energy	E_{on}		-	0.64	0.77	mJ
Turn-off energy	E_{off}		-	0.65	0.85	
Total switching energy	E_{ts}		-	1.29	1.62	

Anti-Parallel Diode Characteristic

Diode reverse recovery time	t_{rr}	$T_j=25^\circ\text{C}$, $V_R=200\text{V}$, $I_F=30\text{A}$, $di_F/dt=200\text{A}/\mu\text{s}$	-	400	-	ns
	t_s		-	32	-	
	t_F		-	368	-	
Diode reverse recovery charge	Q_{rr}		-	610	-	nC
Diode peak reverse recovery current	I_{rrm}		-	5.5	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	180	-	A/ μs

Switching Characteristic, Inductive Load, at $T_j=150^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	$T_j=150^\circ\text{C}$, $V_{CC}=400\text{V}$, $I_C=30\text{A}$, $V_{GE}=0/15\text{V}$, $R_G=11\Omega$, $L_\sigma^{(1)}=180\text{nH}$, $C_\sigma^{(1)}=900\text{pF}$ Energy losses include “tail” and diode reverse recovery.	-	44	53	ns
Rise time	t_r		-	34	40	
Turn-off delay time	$t_{d(off)}$		-	324	389	
Fall time	t_f		-	67	80	
Turn-on energy	E_{on}		-	0.98	1.18	mJ
Turn-off energy	E_{off}		-	0.92	1.19	
Total switching energy	E_{ts}		-	1.90	2.38	

Anti-Parallel Diode Characteristic

Diode reverse recovery time	t_{rr}	$T_j=150^\circ\text{C}$, $V_R=200\text{V}$, $I_F=30\text{A}$, $di_F/dt=200\text{A}/\mu\text{s}$	-	520	-	ns
	t_s		-	56	-	
	t_F		-	464	-	
Diode reverse recovery charge	Q_{rr}		-	1740	-	nC
Diode peak reverse recovery current	I_{rrm}		-	9.0	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	200	-	A/ μs

¹⁾ Leakage inductance L_σ and Stray capacity C_σ due to dynamic test circuit in Figure E.

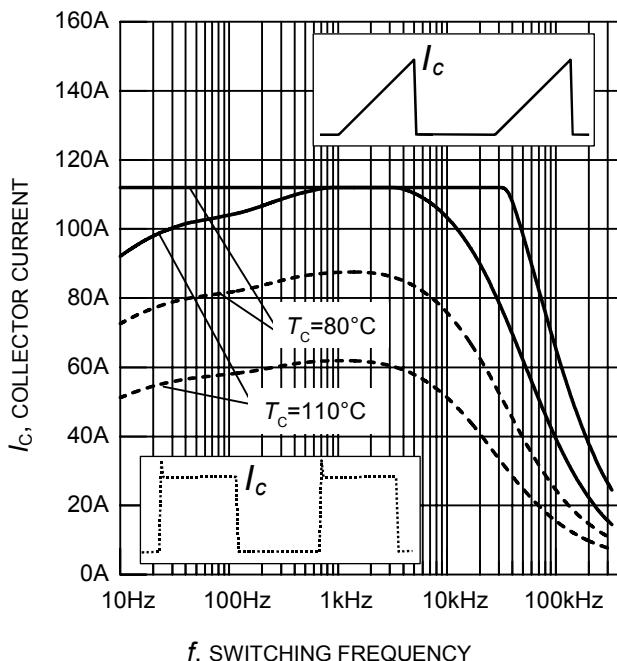


Figure 1. Collector current as a function of switching frequency

($T_j \leq 150^\circ\text{C}$, $D = 0.5$, $V_{CE} = 400\text{V}$,
 $V_{GE} = 0/+15\text{V}$, $R_G = 11\Omega$)

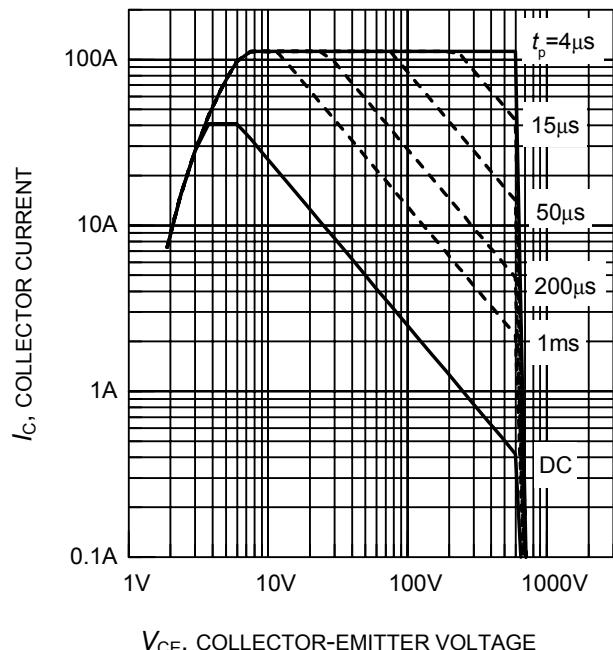


Figure 2. Safe operating area
 $(D = 0, T_C = 25^\circ\text{C}, T_j \leq 150^\circ\text{C})$

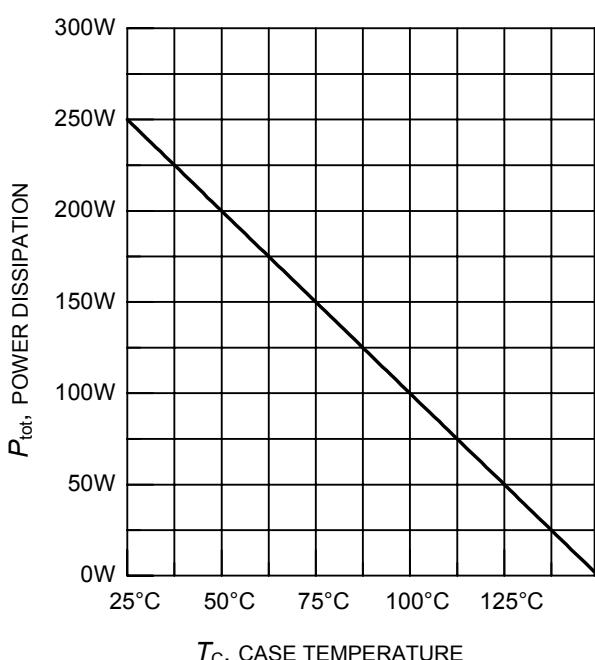


Figure 3. Power dissipation as a function of case temperature
 $(T_j \leq 150^\circ\text{C})$

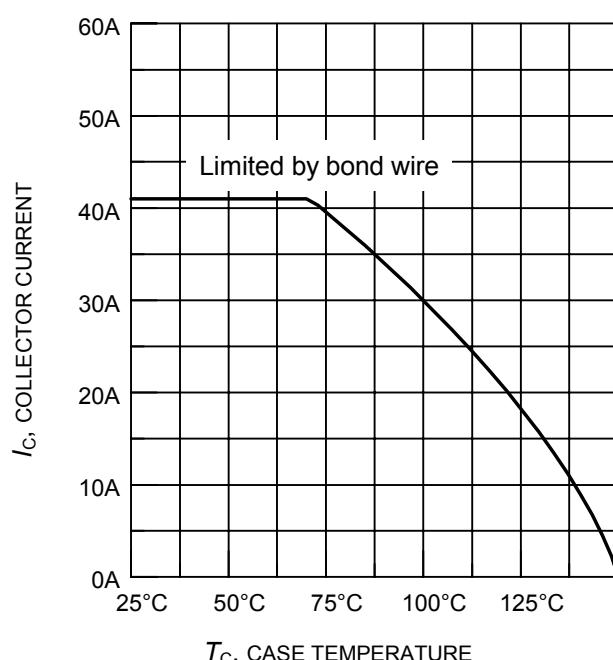


Figure 4. Collector current as a function of case temperature
 $(V_{GE} \leq 15\text{V}, T_j \leq 150^\circ\text{C})$

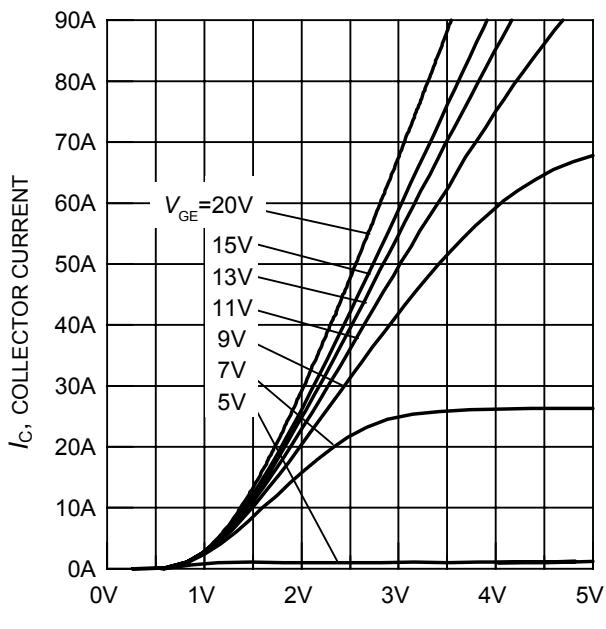


Figure 5. Typical output characteristics
($T_j = 25^\circ\text{C}$)

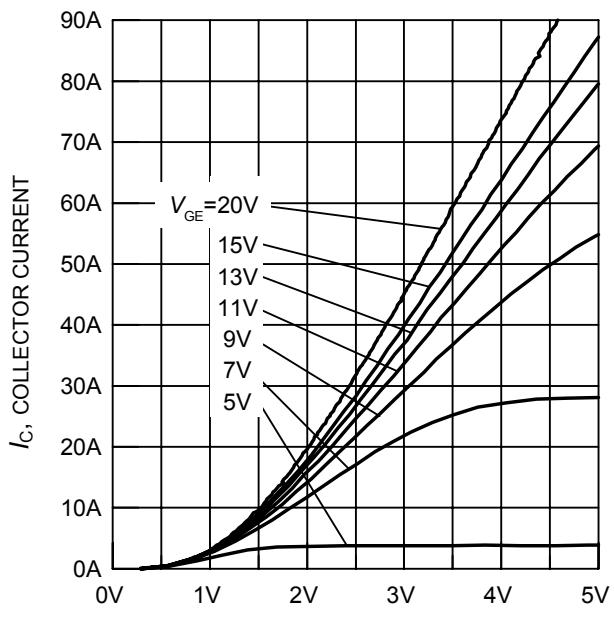


Figure 6. Typical output characteristics
($T_j = 150^\circ\text{C}$)

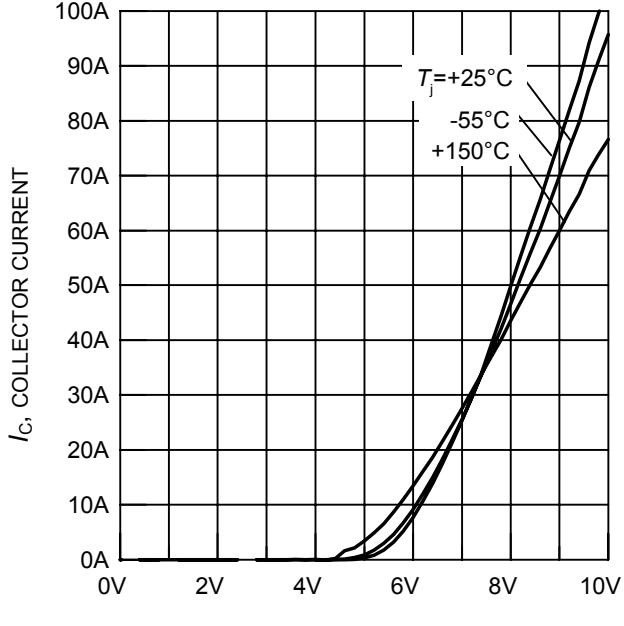


Figure 7. Typical transfer characteristics
($V_{CE} = 10\text{V}$)

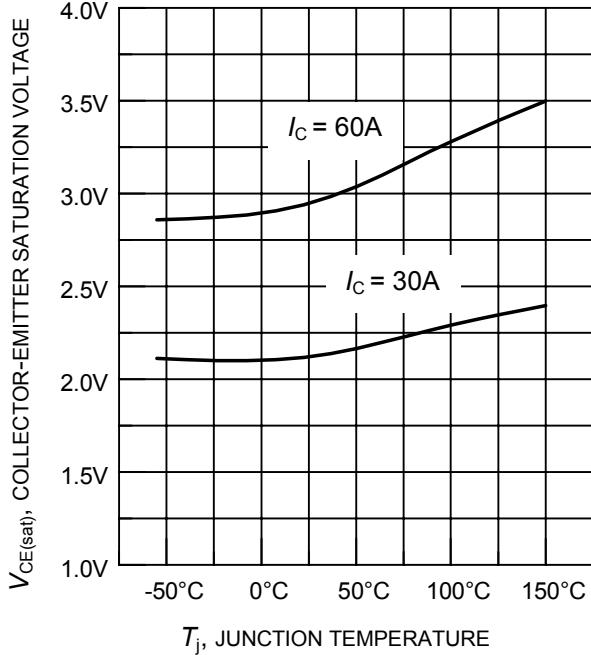


Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature
($V_{GE} = 15\text{V}$)

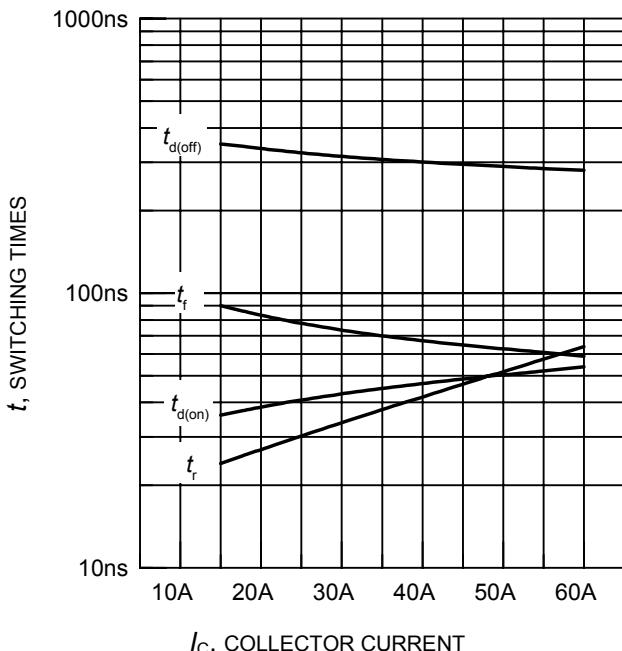


Figure 9. Typical switching times as a function of collector current

(inductive load, $T_j = 150^\circ\text{C}$, $V_{CE} = 400\text{V}$, $V_{GE} = 0/+15\text{V}$, $R_G = 11\Omega$, Dynamic test circuit in Figure E)

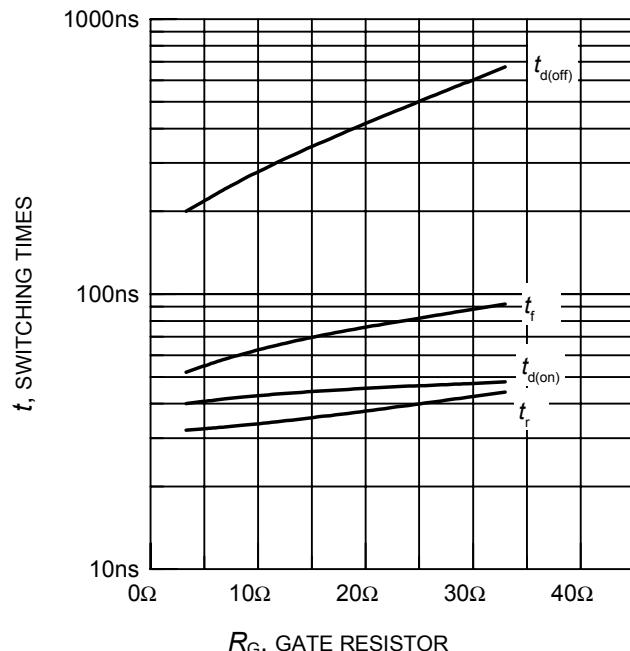


Figure 10. Typical switching times as a function of gate resistor

(inductive load, $T_j = 150^\circ\text{C}$, $V_{CE} = 400\text{V}$, $V_{GE} = 0/+15\text{V}$, $I_C = 30\text{A}$, Dynamic test circuit in Figure E)

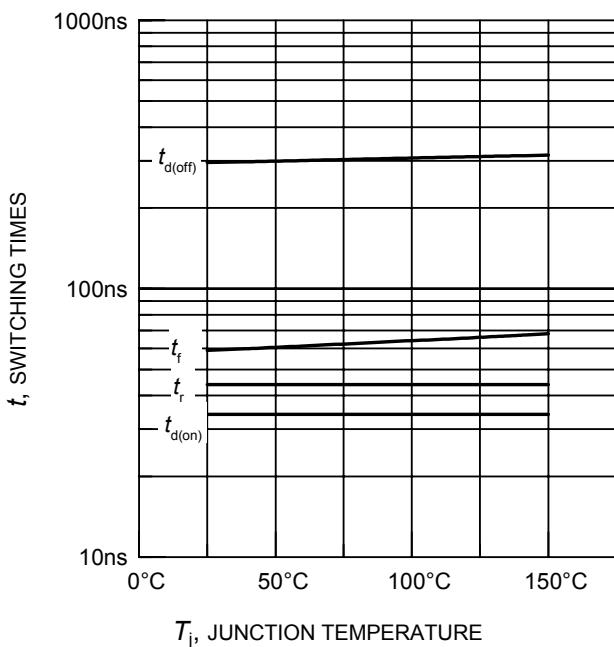


Figure 11. Typical switching times as a function of junction temperature

(inductive load, $V_{CE} = 400\text{V}$, $V_{GE} = 0/+15\text{V}$, $I_C = 30\text{A}$, $R_G = 11\Omega$, Dynamic test circuit in Figure E)

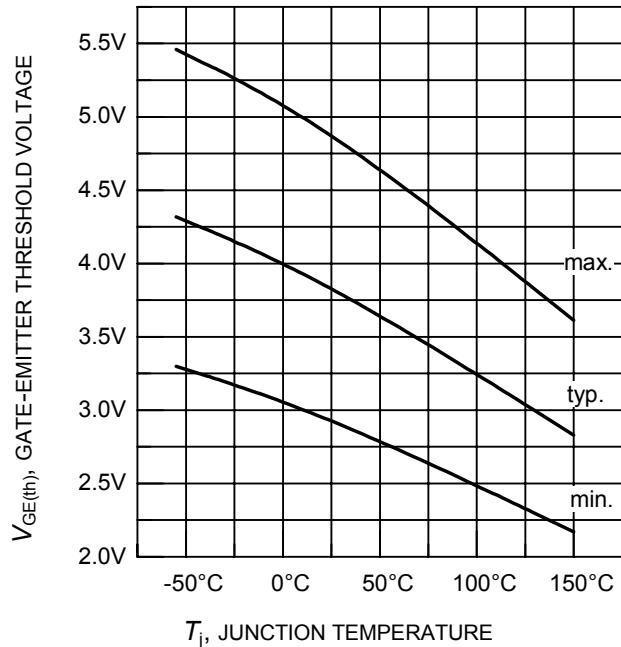


Figure 12. Gate-emitter threshold voltage as a function of junction temperature

($I_C = 0.7\text{mA}$)

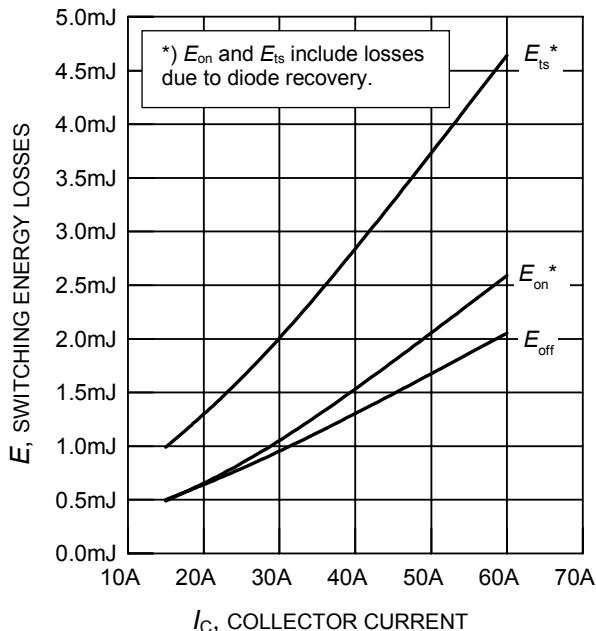


Figure 13. Typical switching energy losses as a function of collector current

(inductive load, $T_j = 150^\circ\text{C}$, $V_{CE} = 400\text{V}$, $V_{GE} = 0/+15\text{V}$, $R_G = 11\Omega$, Dynamic test circuit in Figure E)

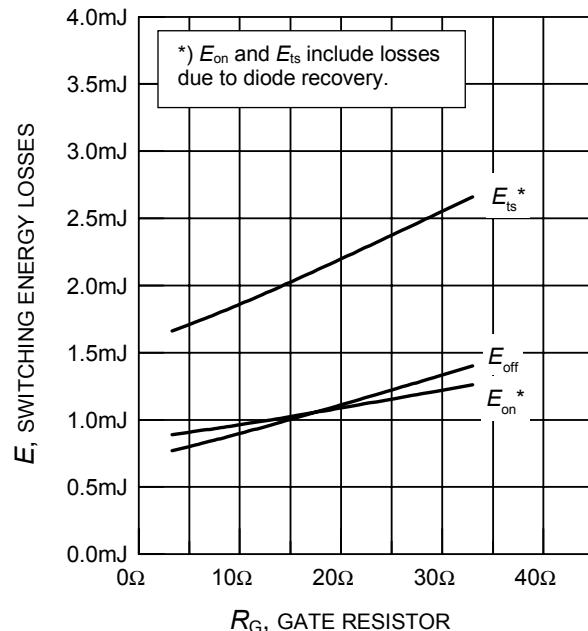


Figure 14. Typical switching energy losses as a function of gate resistor

(inductive load, $T_j = 150^\circ\text{C}$, $V_{CE} = 400\text{V}$, $V_{GE} = 0/+15\text{V}$, $I_C = 30\text{A}$, Dynamic test circuit in Figure E)

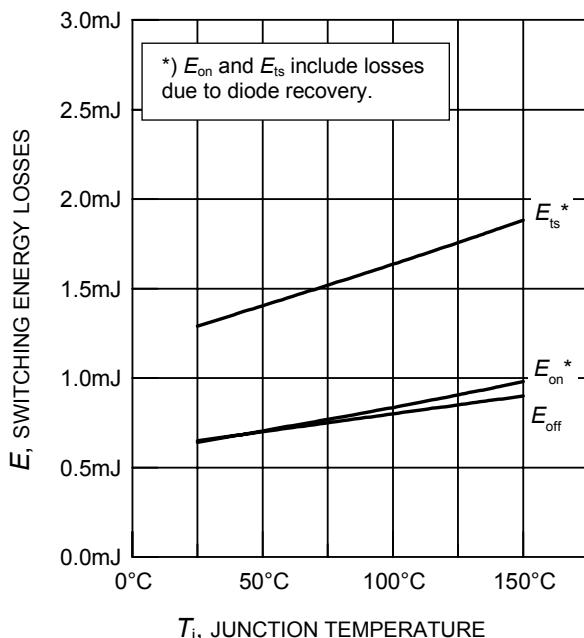


Figure 15. Typical switching energy losses as a function of junction temperature

(inductive load, $V_{CE} = 400\text{V}$, $V_{GE} = 0/+15\text{V}$, $I_C = 30\text{A}$, $R_G = 11\Omega$, Dynamic test circuit in Figure E)

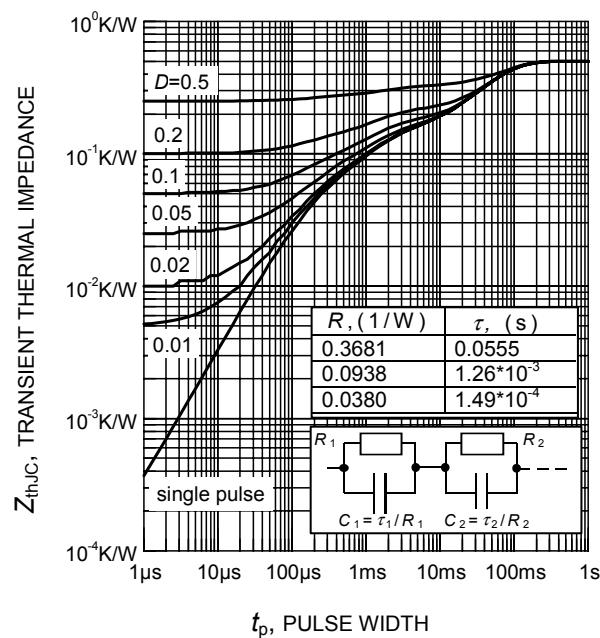


Figure 16. IGBT transient thermal impedance as a function of pulse width

($D = t_p / T$)

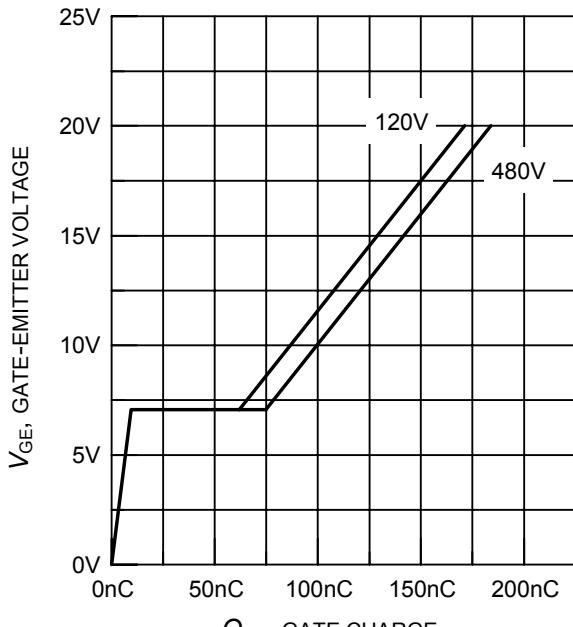


Figure 17. Typical gate charge
($I_C = 30A$)

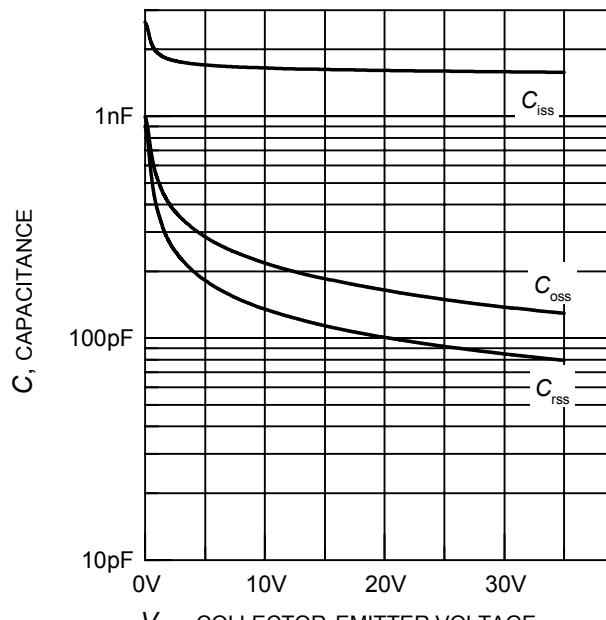


Figure 18. Typical capacitance as a function of collector-emitter voltage
($V_{GE} = 0V, f = 1MHz$)

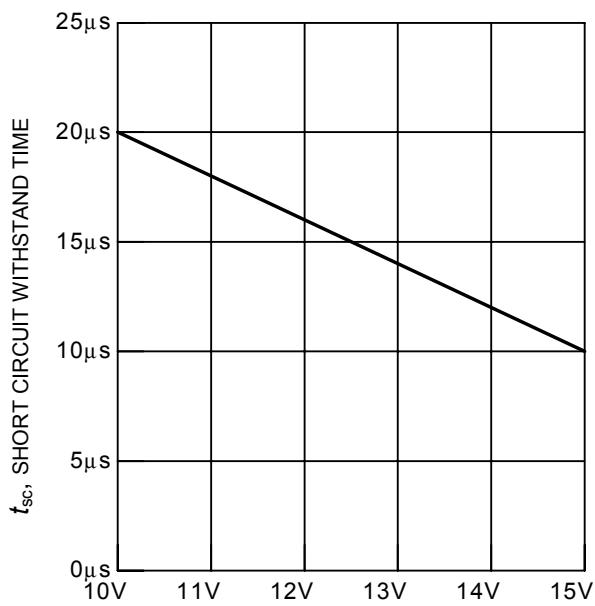


Figure 19. Short circuit withstand time as a function of gate-emitter voltage
($V_{CE} = 600V$, start at $T_j = 25^{\circ}C$)

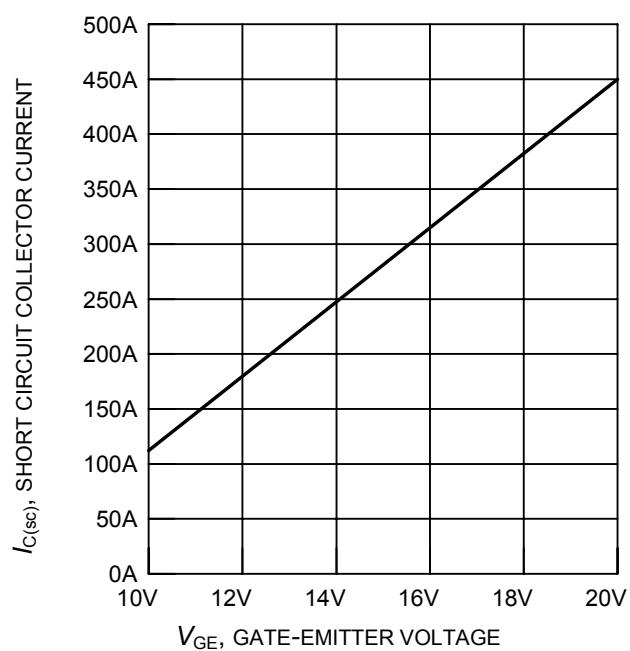


Figure 20. Typical short circuit collector current as a function of gate-emitter voltage
($V_{CE} \leq 600V, T_j = 150^{\circ}C$)

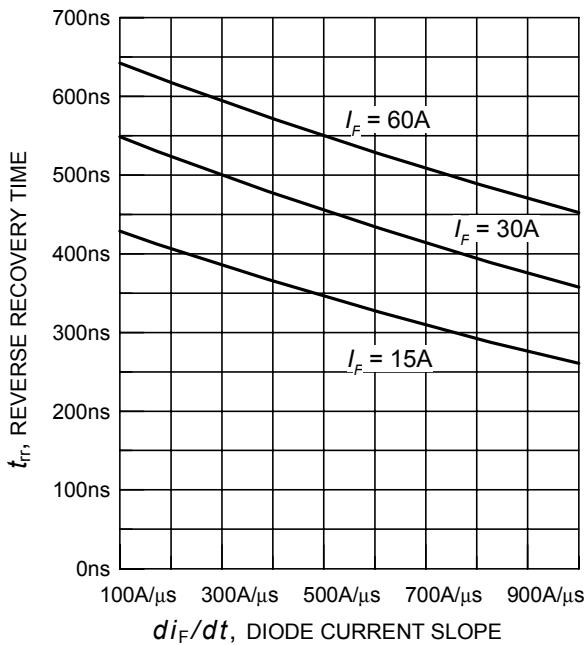


Figure 21. Typical reverse recovery time as a function of diode current slope
 $(V_R = 200V, T_j = 125^{\circ}C,$
 Dynamic test circuit in Figure E)

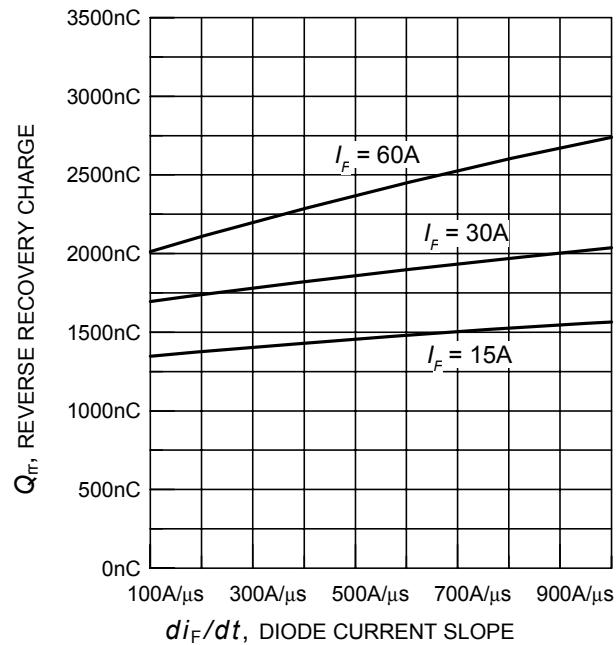


Figure 22. Typical reverse recovery charge as a function of diode current slope
 $(V_R = 200V, T_j = 125^{\circ}C,$
 Dynamic test circuit in Figure E)

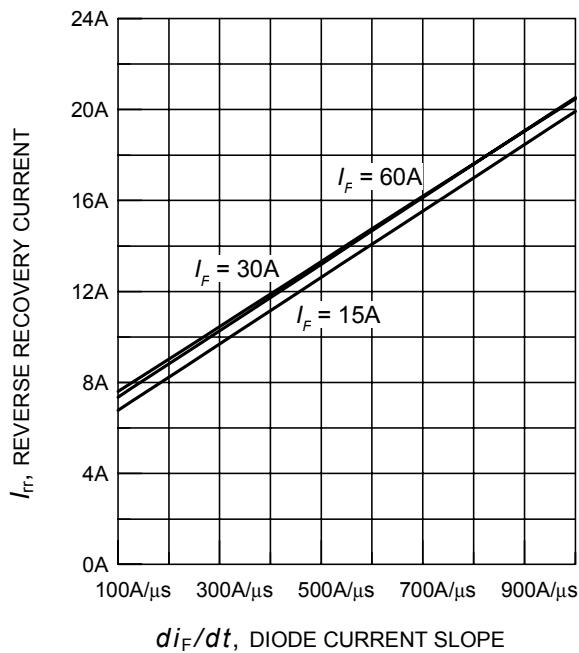


Figure 23. Typical reverse recovery current as a function of diode current slope
 $(V_R = 200V, T_j = 125^{\circ}C,$
 Dynamic test circuit in Figure E)

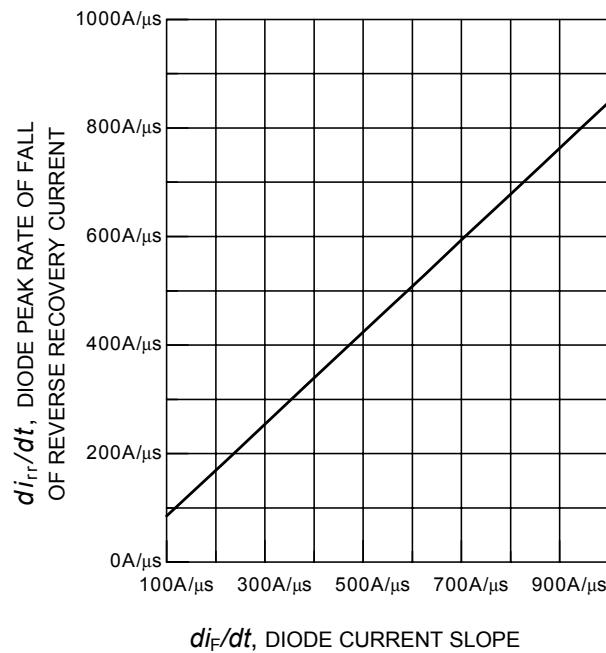


Figure 24. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope
 $(V_R = 200V, T_j = 125^{\circ}C,$
 Dynamic test circuit in Figure E)

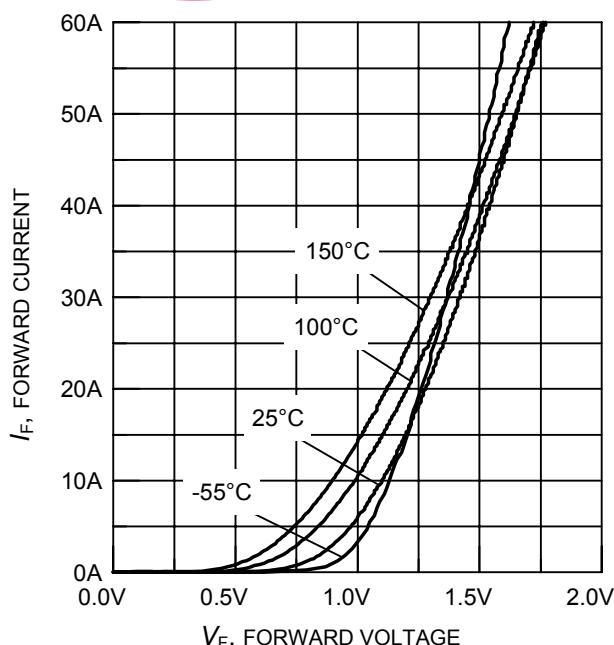


Figure 25. Typical diode forward current as a function of forward voltage

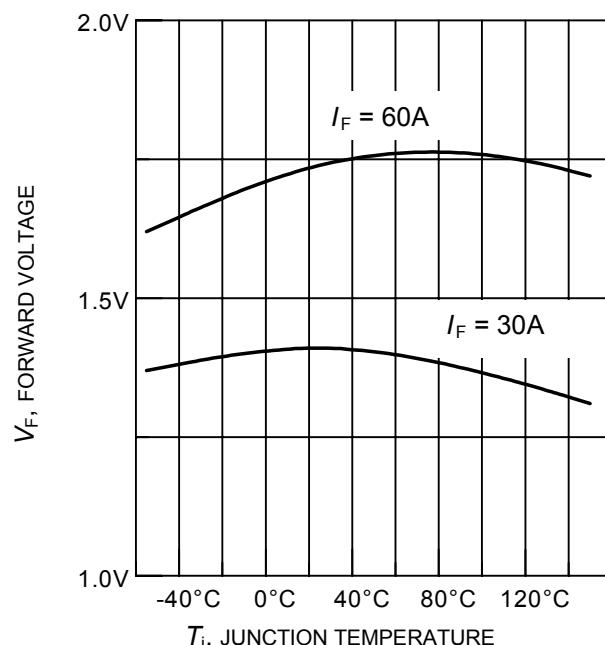


Figure 26. Typical diode forward voltage as a function of junction temperature

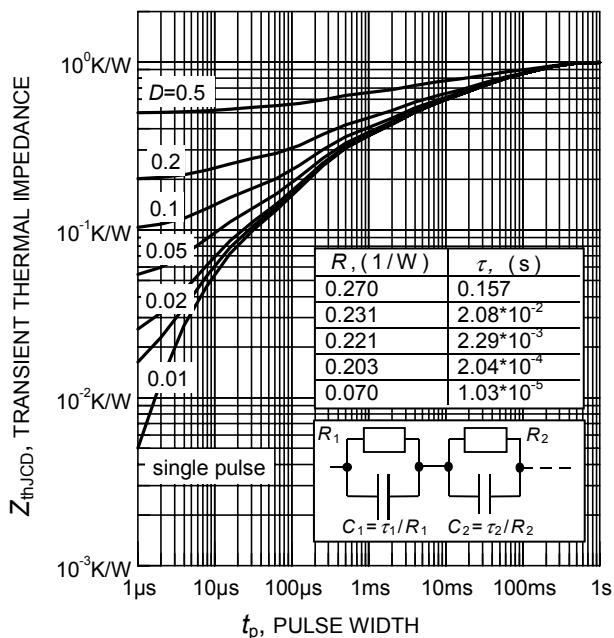
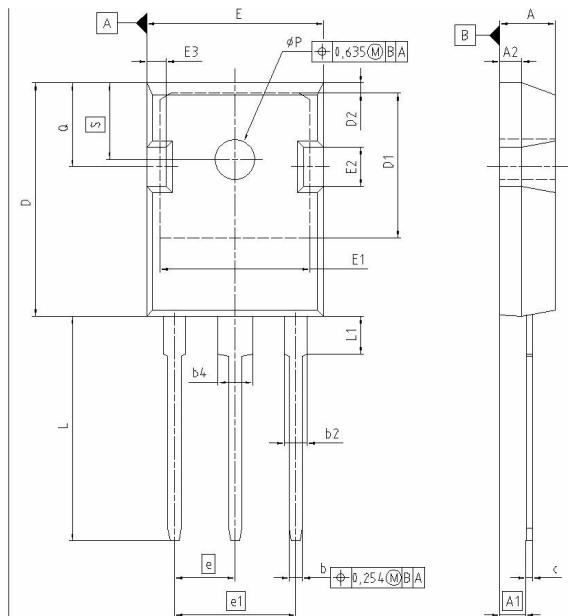


Figure 27. Diode transient thermal impedance as a function of pulse width
(D = t_p / T)

PG-T0247-3-21


DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.903	5.157	0.193	0.203
A1	2.273	2.527	0.092	0.096
A2	1.853	2.107	0.075	0.081
b	1.073	1.327	0.047	0.052
b2	1.903	2.386	0.075	0.094
b4	2.870	3.454	0.113	0.136
c	0.549	0.752	0.024	0.030
D	20.823	21.077	0.820	0.830
D1	17.323	17.831	0.682	0.702
D2	1.063	1.317	0.042	0.052
E	15.773	16.027	0.621	0.631
E1	13.893	14.147	0.547	0.557
E2	3.683	3.937	0.145	0.155
E3	1.683	1.937	0.066	0.076
e	5.450		0.215	
e1	10.900		0.430	
N	3		3	
L	20.053	20.307	0.789	0.799
L1	4.168	4.472	0.164	0.176
pP	3.559	3.661	0.140	0.144
Q	5.493	5.747	0.216	0.226
S	6.043	6.297	0.238	0.248

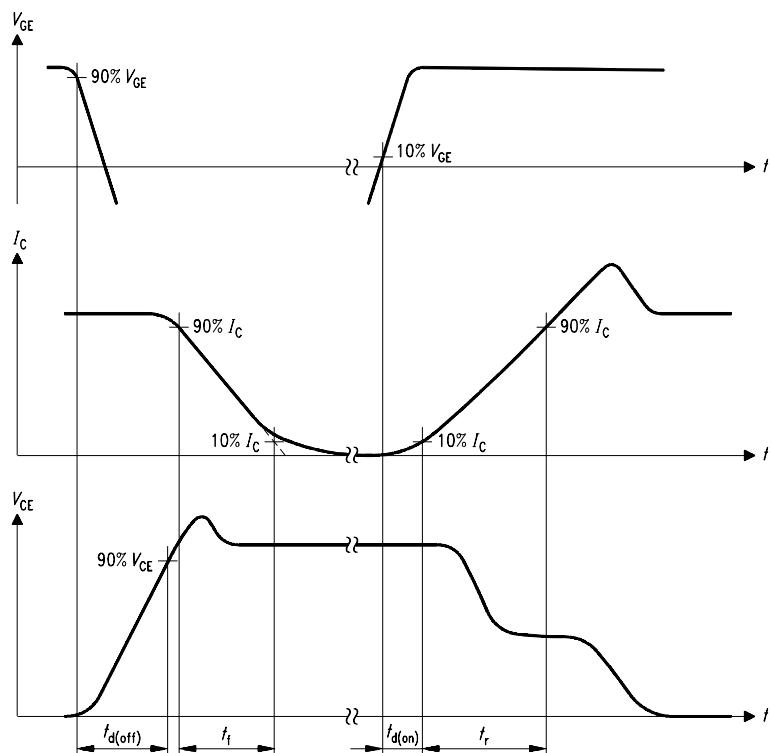


Figure A. Definition of switching times

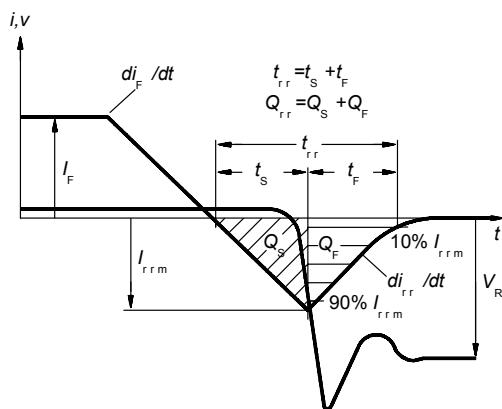


Figure C. Definition of diodes switching characteristics

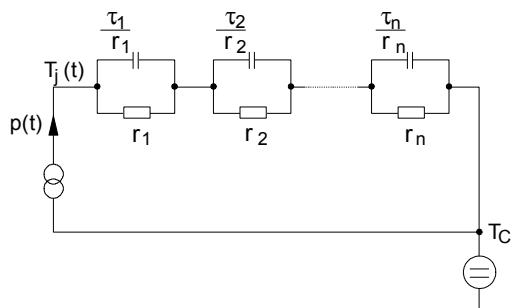


Figure D. Thermal equivalent circuit

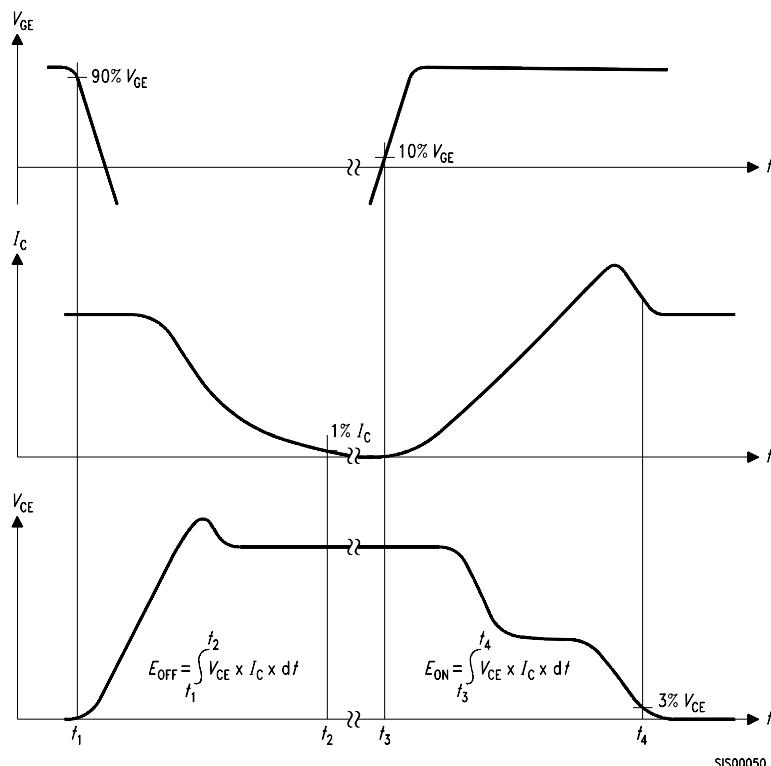


Figure B. Definition of switching losses

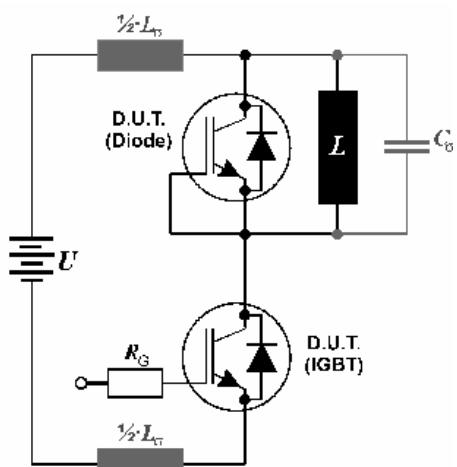


Figure E. Dynamic test circuit
Leakage inductance $L_\sigma = 180\text{nH}$ and Stray capacity $C_\sigma = 900\text{pF}$.



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